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8	117	((365/$).CCLS.) and ((flash or eeprom or eprom) same ((time$1 or timestamp$4 or date$1 or duration$1) near8 (table$1 or director$4)))	USPAT; US-PGPUB	2004/08/11 10:17
11	48	(((365/$).CCLS.) and ((flash or eeprom or eprom) same ((time$1 or timestamp$4 or date$1 or duration$1) near8 (table$1 or director$4)))) and @ad<20000606	USPAT; US-PGPUB	2004/08/11 10:24
12	30	((((365/$).CCLS.) and ((flash or eeprom or eprom) same ((time$1 or timestamp$4 or date$1 or duration$1) near8 (table$1 or director$4)))) and @ad<20000606) not ((((711/$).CCLS.) and ((flash or eeprom or eprom) same ((time$1 or timestamp$4 or date$1 or duration$1) near8 (table$1 or director$4)))) and @ad<20000606)	USPAT; US-PGPUB	2004/08/11 10:18
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1 **Self-calibration/compensation technique for microcontroller-based sensor arrays**
Kolen, P.T.;
Instrumentation and Measurement, IEEE Transactions on , Volume: 43 , Issu 4 , Aug. 1994
Pages:620 - 623

[Abstract] [PDF Full-Text (396 KB)] **IEEE JNL**

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2 **Performance comparison of logical-address-to-physical-address algorithms for non-volatile memory**
Yang Jan-Ti; Sheng-Zhong Shieh; Jun-Ming Yu;
ASIC, 2003. Proceedings. 5th International Conference on , Volume: 1 , 21-2 2003
Pages:482 - 485 Vol.1

[Abstract] [PDF Full-Text (302 KB)] **IEEE CNF**

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3 **A reprogrammable hardware fuzzy controller for the battery chargi process**
Sepulveda C, R.; Montiel R, O.; Castillo, O.; Melin, P.;
Fuzzy Systems, 2003. FUZZ '03. The 12th IEEE International Conference on , Volume: 2 , 25-28 May 2003
Pages:1008 - 1013 vol.2

[Abstract] [PDF Full-Text (415 KB)] **IEEE CNF**

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4 **Real time speech processing to eliminate slamdowns in digital voice systems**
Harrison, C.G.M.; Javed, M.A.; Wolanski, P.;
Artificial Neural Networks, 1995., Fourth International Conference on , 26-28 1995
Pages:19 - 23

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1 Pen computing: a technology overview and a vision

André Meyer

July 1995 **ACM SIGCHI Bulletin**, Volume 27 Issue 3

Full text available: pdf(5.14 MB) Additional Information: full citation, abstract, citings, index terms

This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine, picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions.Starting with a short historic ...

2 Practical byzantine fault tolerance and proactive recovery

Miguel Castro, Barbara Liskov

November 2002 **ACM Transactions on Computer Systems (TOCS)**, Volume 20 Issue 4

Full text available: pdf(1.63 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Our growing reliance on online services accessible on the Internet demands highly available systems that provide correct service without interruptions. Software bugs, operator mistakes, and malicious attacks are a major cause of service interruptions and they can cause arbitrary behavior, that is, Byzantine faults. This article describes a new replication algorithm, BFT, that can be used to build highly available systems that tolerate Byzantine faults. BFT can be used in practice to implement re ...

Keywords: Byzantine fault tolerance, asynchronous systems, proactive recovery, state machine replication, state transfer

3 Memory hierarchy: Compiler-decided dynamic memory allocation for scratch-pad based embedded systems

Sumesh Udayakumaran, Rajeev Barua

October 2003 **Proceedings of the international conference on Compilers, architectures and synthesis for embedded systems**

Full text available: pdf(213.48 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a highly predictable, low overhead and yet dynamic, memory allocation strategy for embedded systems with scratch-pad memory. A *scratch-pad* is a fast compiler-managed SRAM memory that replaces the hardware-managed cache. It is motivated by its better real-time guarantees vs cache and by its significantly lower overheads in energy

consumption, area and overall runtime, even with a simple allocation scheme [4].Existing scratch-pad allocation methods are of two types. Firs ...

Keywords: compiler, embedded systems, memory allocation, scratch-pad

4 Integrating performance monitoring and communication in parallel computers

Margaret Martonosi, David Ofelt, Mark Heinrich

May 1996 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1996 ACM SIGMETRICS international conference on Measurement and modeling of computer systems**, Volume 24 Issue 1

Full text available: pdf(1.49 MB) Additional Information: full citation, abstract, references, citings, index terms

A large and increasing gap exists between processor and memory speeds in scalable cache-coherent multiprocessors. To cope with this situation, programmers and compiler writers must increasingly be aware of the memory hierarchy as they implement software. Tools to support memory performance tuning have, however, been hobbled by the fact that it is difficult to observe the caching behavior of a running program. Little hardware support exists specifically for observing caching behavior; furthermore ...

5 Security as a new dimension in embedded system design: Security as a new dimension in embedded system design

Srivaths Ravi, Paul Kocher, Ruby Lee, Gary McGraw, Anand Raghunathan

June 2004 **Proceedings of the 41st annual conference on Design automation**

Full text available: pdf(209.10 KB) Additional Information: full citation, abstract, references, index terms

The growing number of instances of breaches in information security in the last few years has created a compelling case for efforts towards secure electronic systems. Embedded systems, which will be ubiquitously used to capture, store, manipulate, and access data of a sensitive nature, pose several unique and interesting security challenges. Security has been the subject of intensive research in the areas of cryptography, computing, and networking. However, despite these efforts, *security is ...*

***Keywords**: PDAs, architectures, battery life, cryptography, design, design methodologies, digital rights management, embedded systems, performance, security, security processing, security protocols, sensors, software attacks, tamper resistance, trusted computing, viruses*

6 Constraint-based tools for building user interfaces

Alan Borning, Robert Duisberg

October 1986 **ACM Transactions on Graphics (TOG)**, Volume 5 Issue 4

Full text available: pdf(2.31 MB) Additional Information: full citation, abstract, references, citings, index terms

A constraint describes a relation that must be maintained. Constraints provide a useful mechanism to aid in the construction of interactive graphical user interfaces. They can be used to maintain consistency between data and a view of the data, to maintain consistency among multiple views, to specify layout, and to specify relations between events and responses for describing animations of interactive systems and event-driven simulations. Object-oriented techniques for constraint representa ...

7 Session 7: Squirrel: a decentralized peer-to-peer web cache

Sitaram Iyer, Antony Rowstron, Peter Druschel

July 2002 **Proceedings of the twenty-first annual symposium on Principles of distributed computing**

Full text available: pdf(1.22 MB) Additional Information: full citation, abstract, references, citings

This paper presents a decentralized, peer-to-peer web cache called Squirrel. The key idea is to enable web browsers on desktop machines to share their local caches, to form an efficient

and scalable web cache, without the need for dedicated hardware and the associated administrative cost. We propose and evaluate decentralized web caching algorithms for Squirrel, and discover that it exhibits performance comparable to a centralized web cache in terms of hit ratio, bandwidth usage and latency. It ...

8 Performance monitoring in a Myrinet-connected SHRIMP cluster

Cheng Liao, Margaret Martonosi, Douglas W. Clark

August 1998 **Proceedings of the SIGMETRICS symposium on Parallel and distributed tools**

Full text available: pdf(1.26 MB) Additional Information: full citation, references, citings, index terms

9 Articles: The cougar approach to in-network query processing in sensor networks

Yong Yao, Johannes Gehrke

September 2002 **ACM SIGMOD Record**, Volume 31 Issue 3

Full text available: pdf(988.47 KB) Additional Information: full citation, abstract, references, citings

The widespread distribution and availability of small-scale sensors, actuators, and embedded processors is transforming the physical world into a computing platform. One such example is a sensor network consisting of a large number of sensor nodes that combine physical sensing capabilities such as temperature, light, or seismic sensors with networking and computation capabilities. Applications range from environmental control, warehouse inventory, and health care to military environments. Existi ...

10 Reflection as a mechanism for software integrity verification

Diomidis Spinellis

February 2000 **ACM Transactions on Information and System Security (TISSEC)**, Volume 3 Issue 1

Full text available: pdf(85.99 KB) Additional Information: full citation, abstract, references, index terms, review

The integrity verification of a device's controlling software is an important aspect of many emerging information appliances. We propose the use of reflection, whereby the software is able to examine its own operation, in conjunction with cryptographic hashes as a basis for developing a suitable software verification protocol. For more demanding applications meta-reflective techniques can be used to thwart attacks based on device emulation strategies. We demonstrate how our approach can be ...

Keywords: cryptographic hash function, embedded device, message digest

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